

Dr. Mark Seager

Bio:

Mark leads Scale out strategy for Intel's Rackscale Design Direct Group and directs Intel's technical engagement with Open Computing Project (OCP). At Intel he is working on an ecosystem approach to develop and build HPC and cloud systems with Exascale capabilities. Mark was instrumental in the development of Intel's Scalable System Framework.

Before moving to Intel, he was assistant department head for Advanced Computing Technology within the Integrated Computing and Communications department at Lawrence Livermore National Laboratory. He joined LLNL in 1983 and has been working in parallel processing ever since.

Mark was instrumental in the development and deployment of the BlueGene/L and Purple systems at LLNL for the Advanced Simulation and Computing program within the USA National Nuclear Security Administration. He was also instrumental in the development and deployment of ASC White and Blue-Pacific systems and numerous world class Linux clusters.

He has won numerous awards including the prestigious Edward Teller Award for "major contributions to the state-of-the-art in high performance computing." In 2015 he was promoted to Intel Fellow. Mark and Jan Seager breed and raise Arabian horses, with one national championship win in 2013.

He received a BS degree in mathematics and astrophysics from the University of New Mexico at Albuquerque and a PhD in numerical analysis from the University of Texas at Austin.